

Attorney Docket No.: SLKN-001/03US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of Kai-Yeung (Sunny) SIU, et al.

Serial No.: 09/939,464

Examiner: Not assigned

Confirmation No.: 3661

Art Unit: 2681

Filed: August 24, 2001

For: **OUTPUT QUEUED SWITCH WITH A PARALLEL SHARED MEMORY,
AND METHOD OF OPERATING SAME**

Commissioner for Patents
Washington, D.C. 20231

SUPPLEMENTAL PRELIMINARY AMENDMENT

Prior to the examination of the above-identified application, please amend the above-captioned application as follows:

IN THE CLAIMS:

Please add the following new claims:

51. An integrated circuit for use in a network switch, comprising:
- an input port for receiving inbound data from one or more input terminals;
 - an output port for transmitting outbound data to one or more output terminals;
 - a memory coupled to the input port and configured to store inbound data received at the input port;
 - a module logic circuit coupled to the memory and being enabled to cause the integrated circuit to function as one of an input layer chip, an intermediate layer chip and an output layer chip; and
 - a scheduler coupled to the module logic and configured to selectively store data in the memory based at least in part on the integrated circuit function.

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